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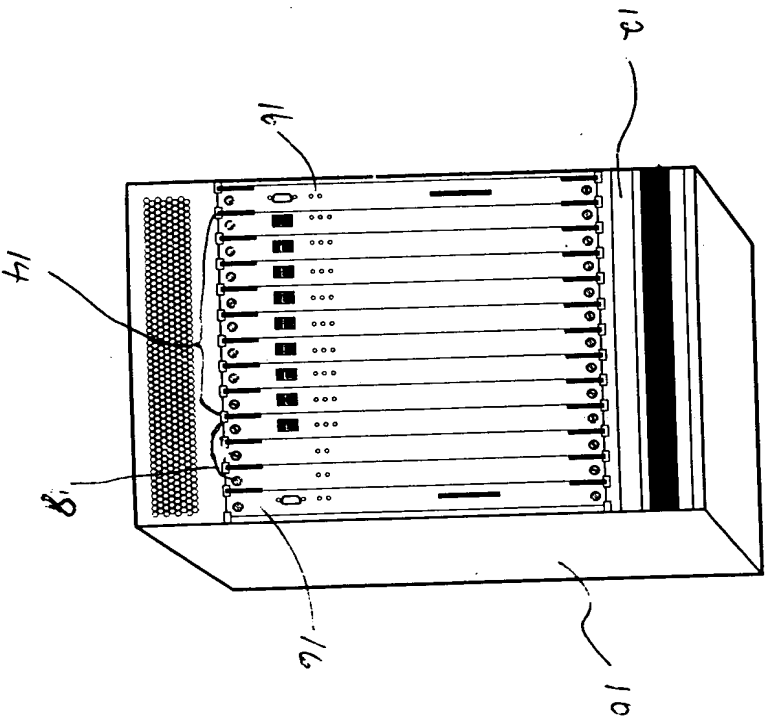


FIG. 1
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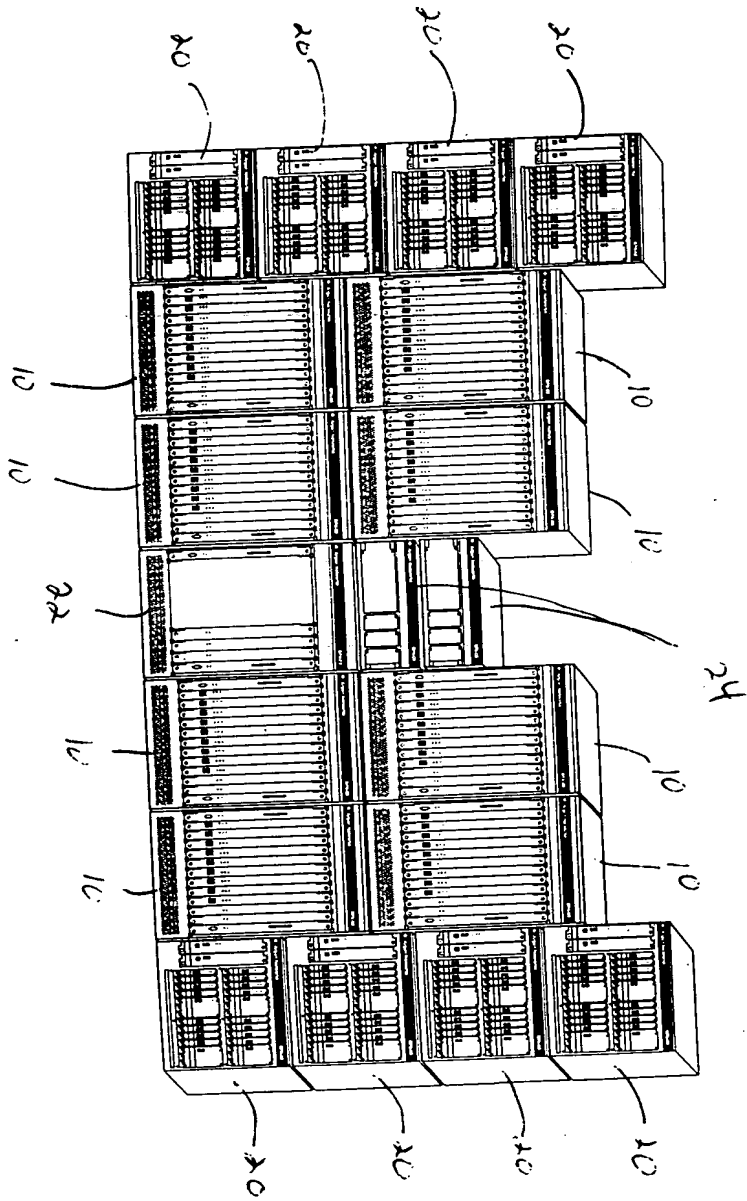


FIG. 2 is a perspective view of the device 10, showing a multi-bay configuration.

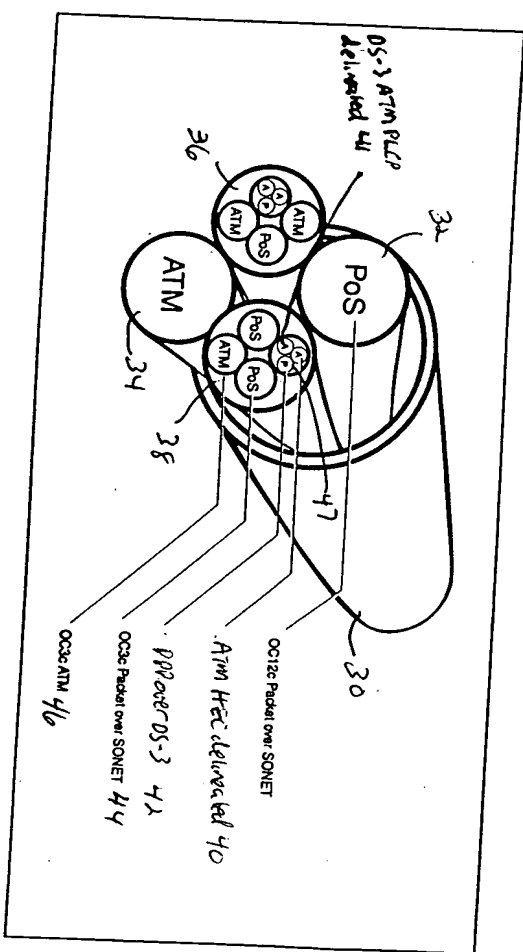


Figure 3

FIG. 3 is a block diagram of a network architecture showing connections between POS and ATM nodes.

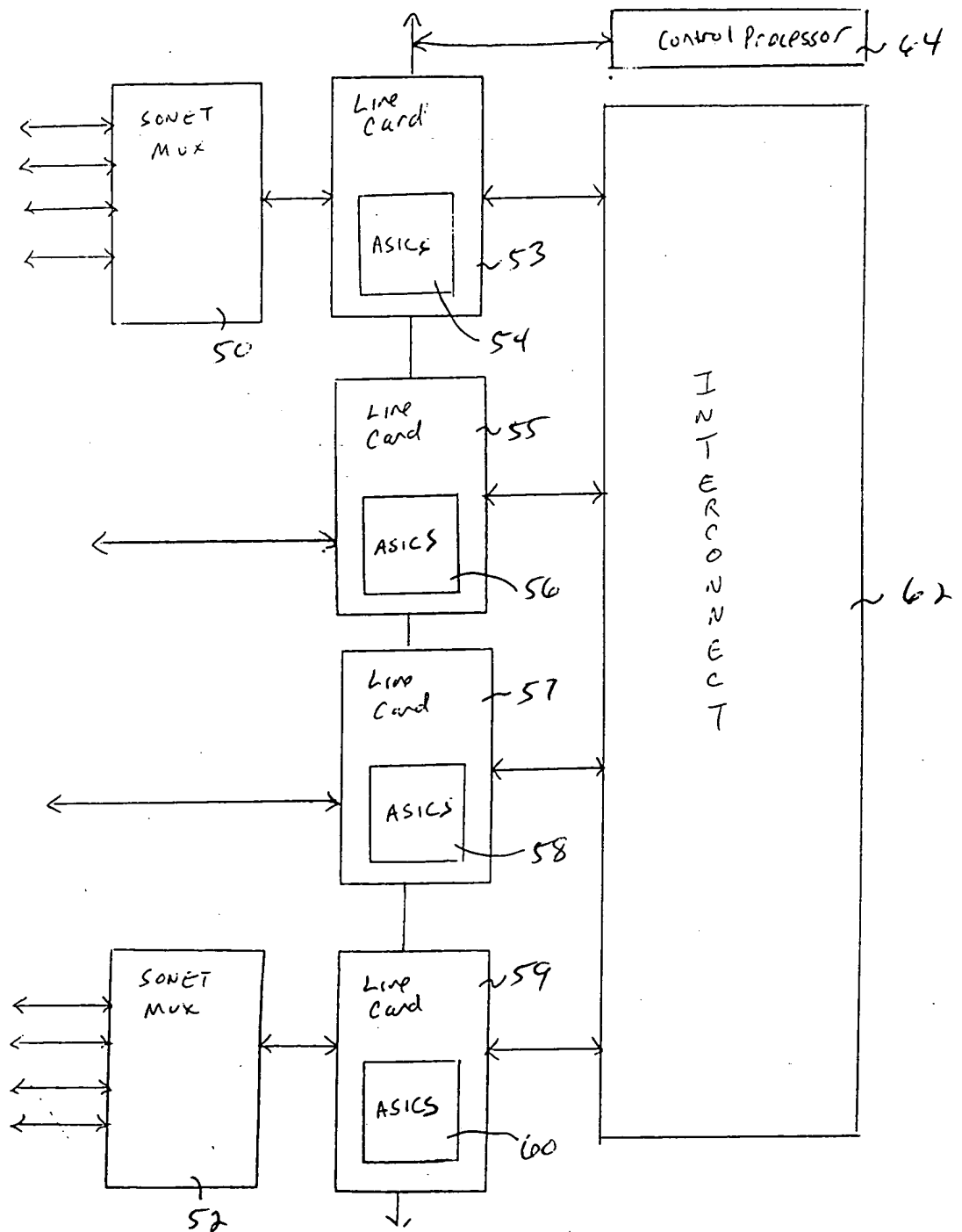


Figure 4

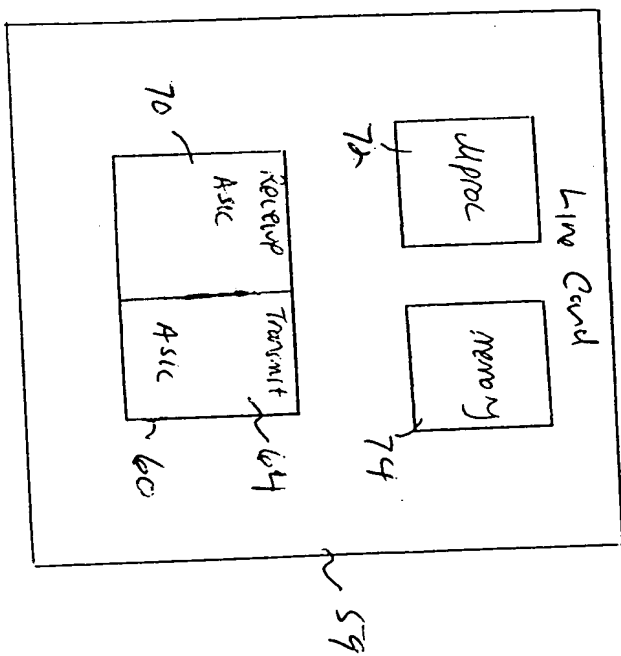
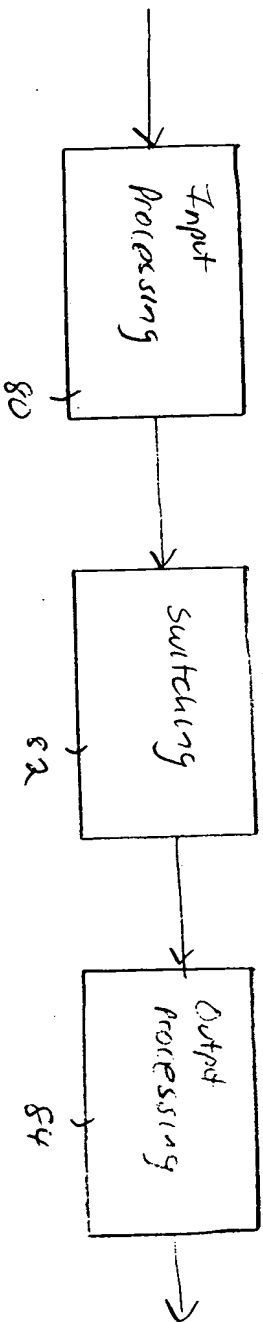


Figure 5



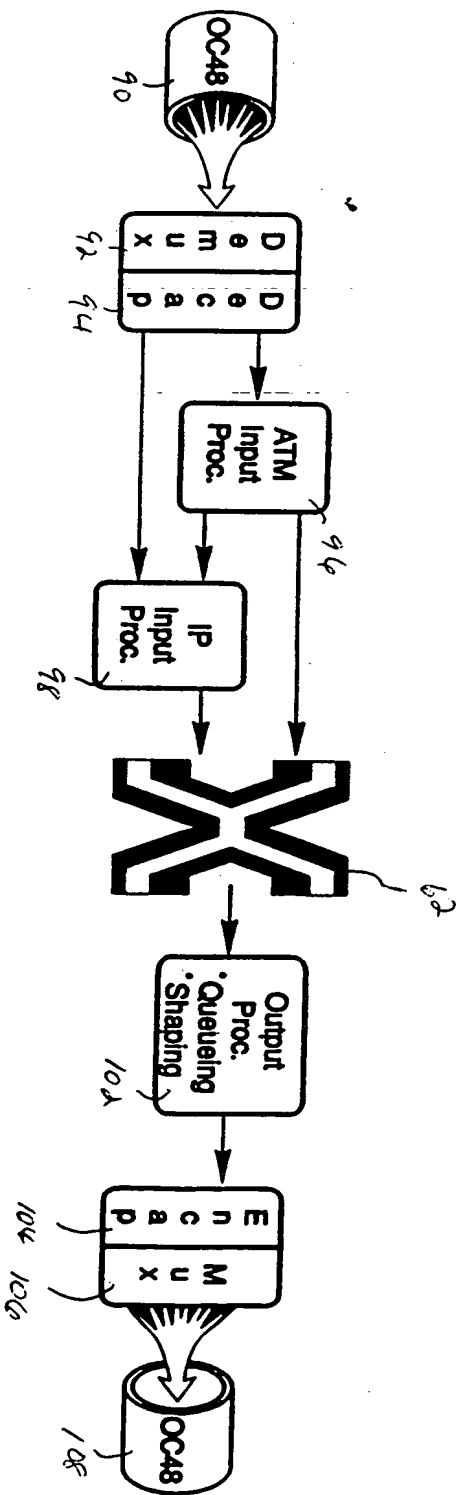


Figure 7

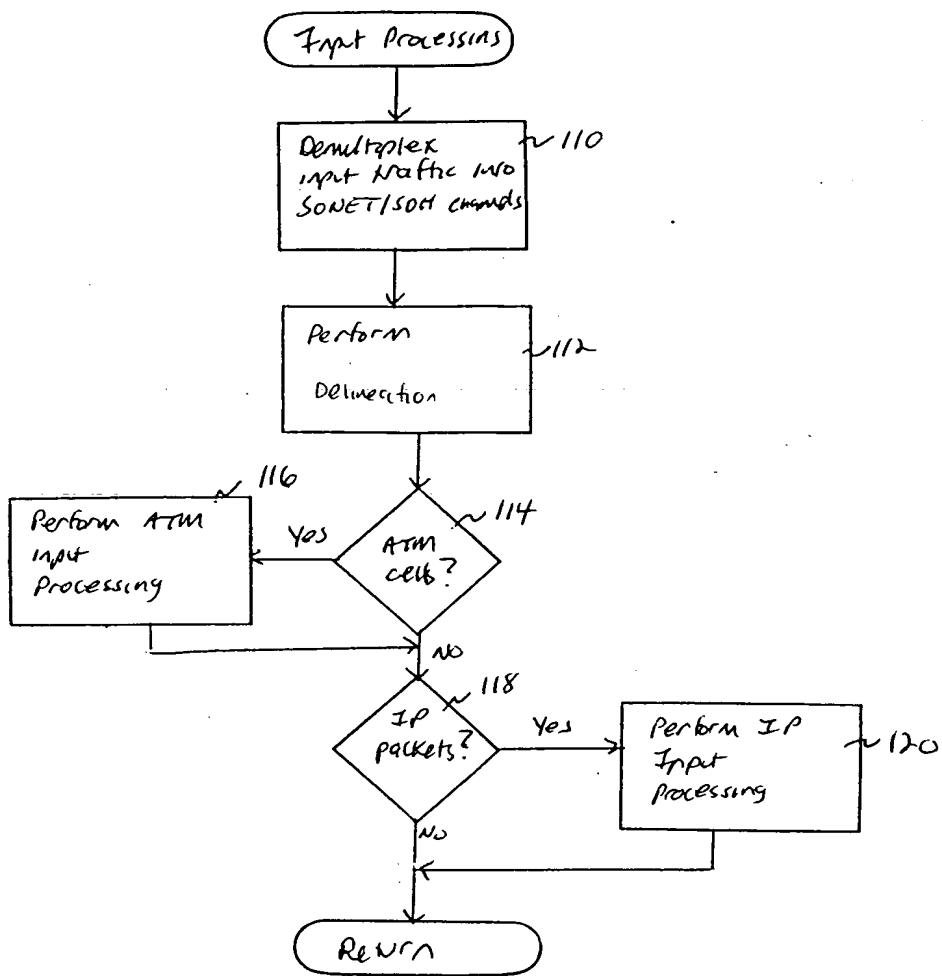


Figure 8

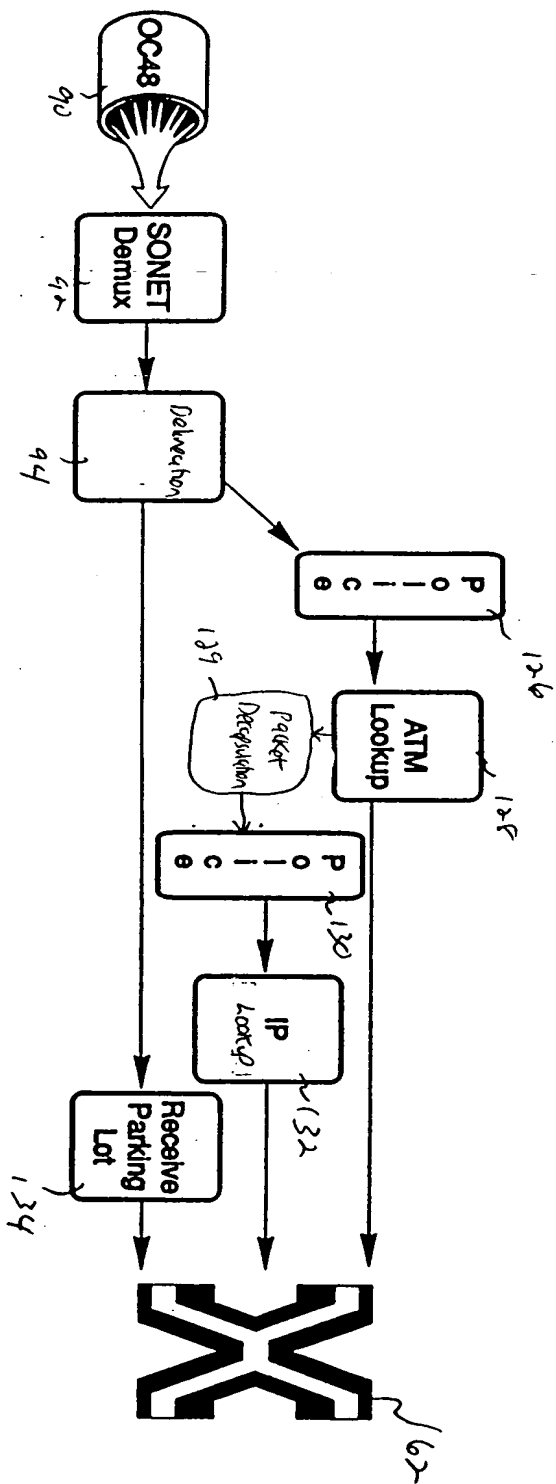


Figure 9

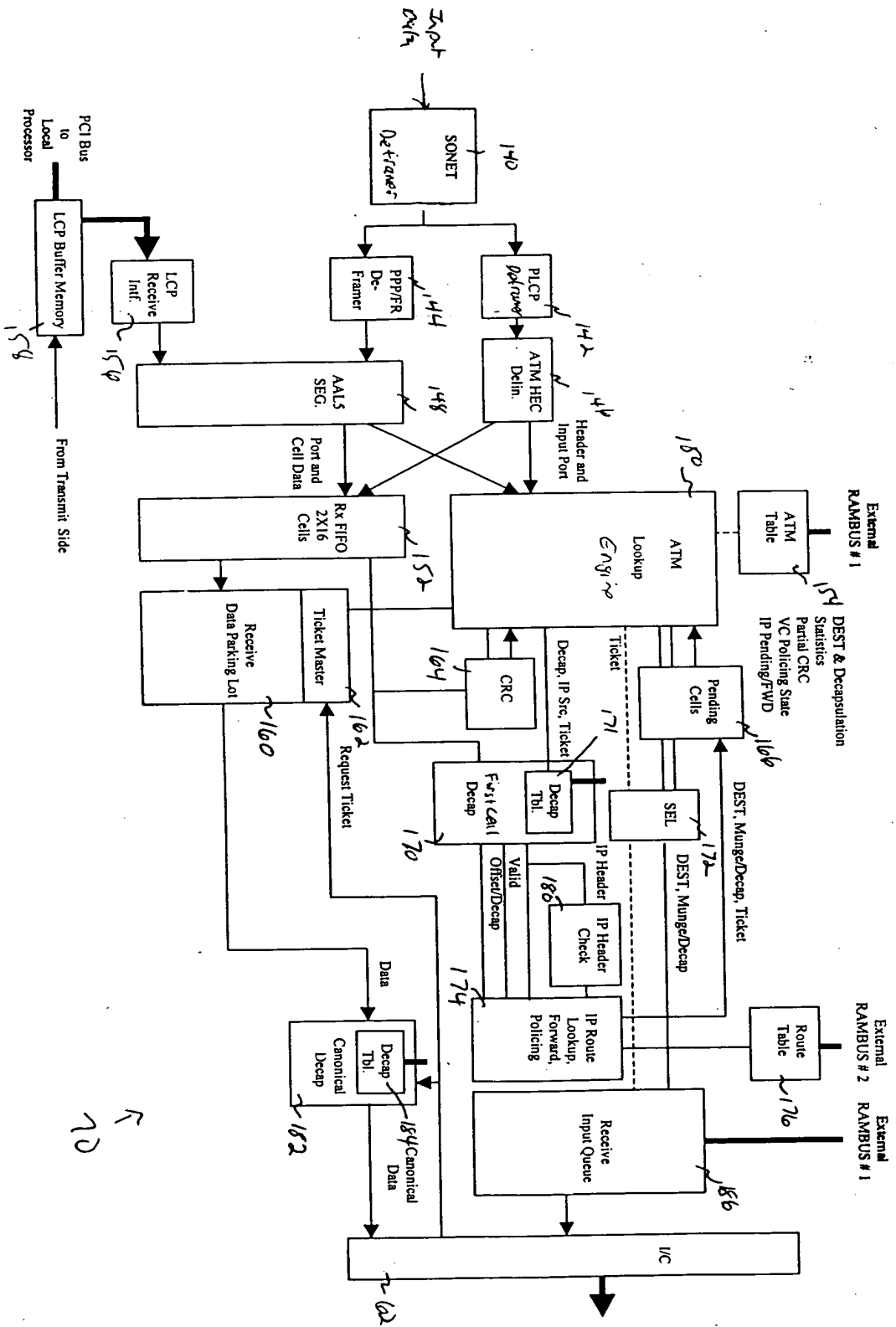


Figure 10

FIG. 10 is a block diagram of a network router architecture.

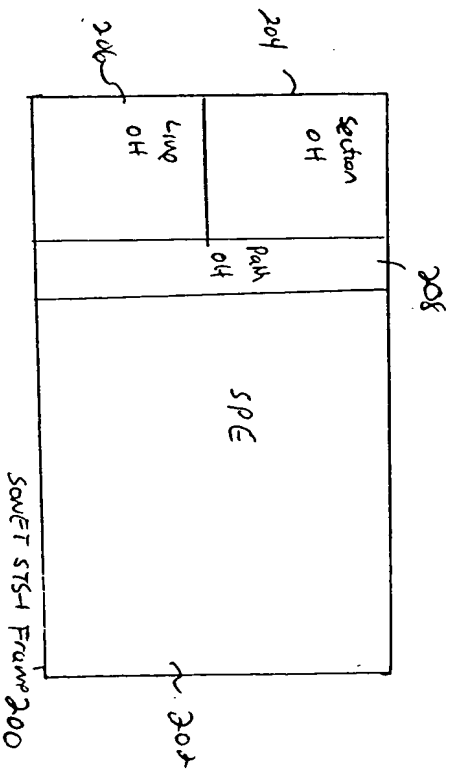


Figure 11

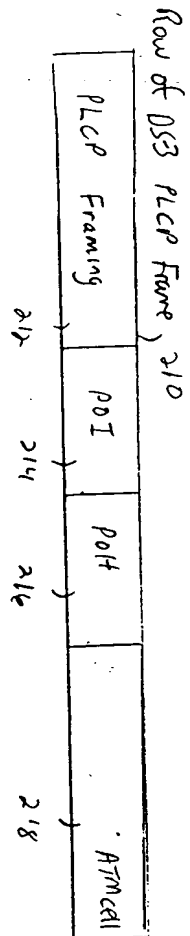


Figure 12

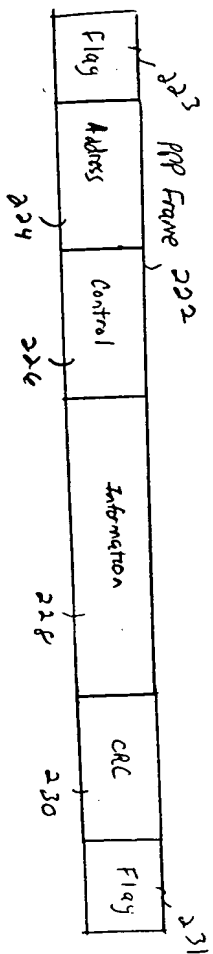
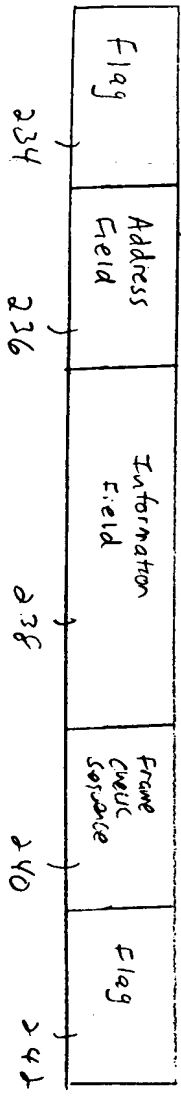


Figure 13

Frame Relay frame 232



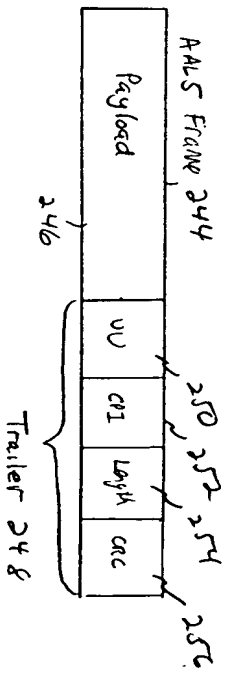


Figure 15

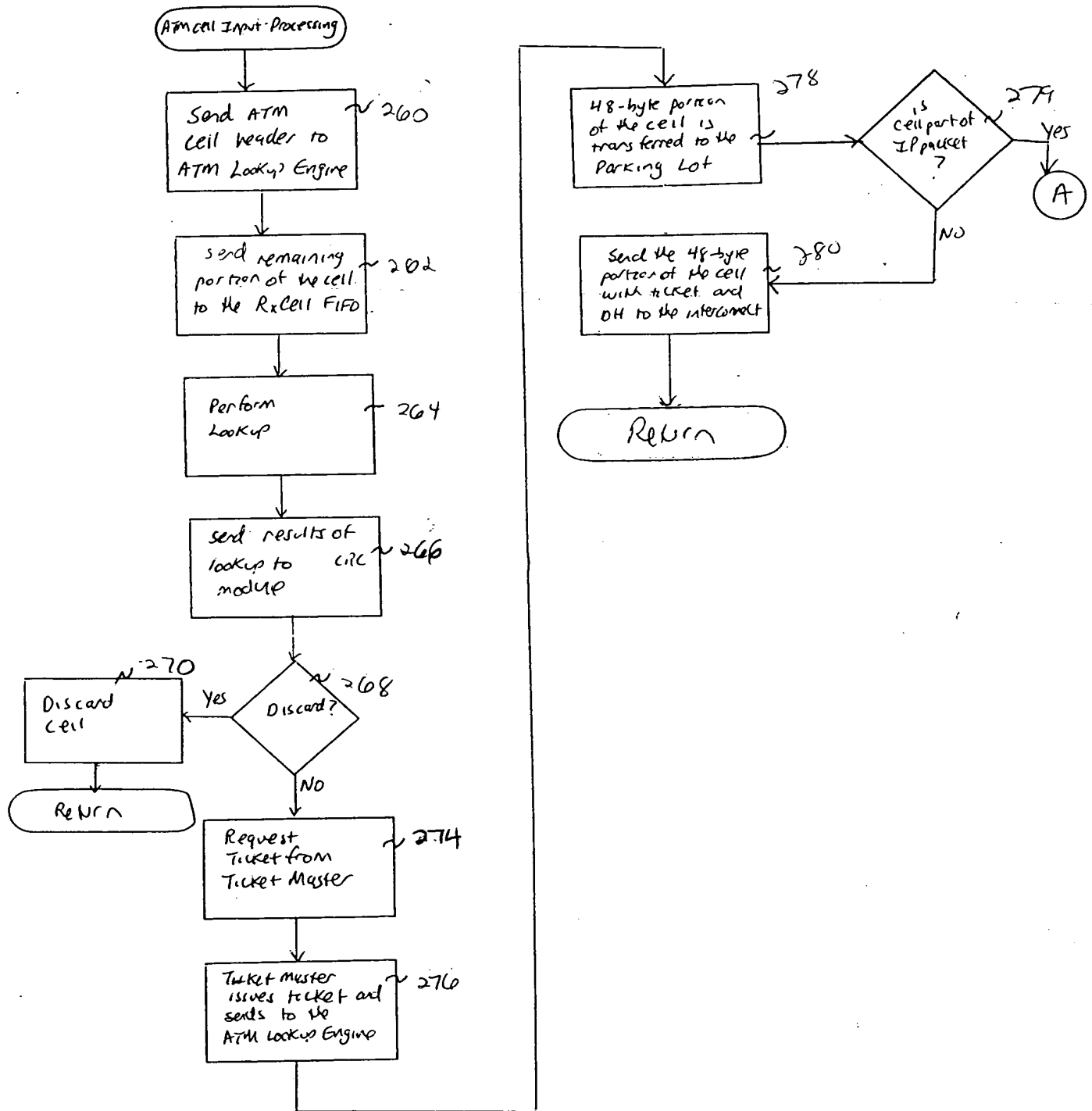


Figure 16

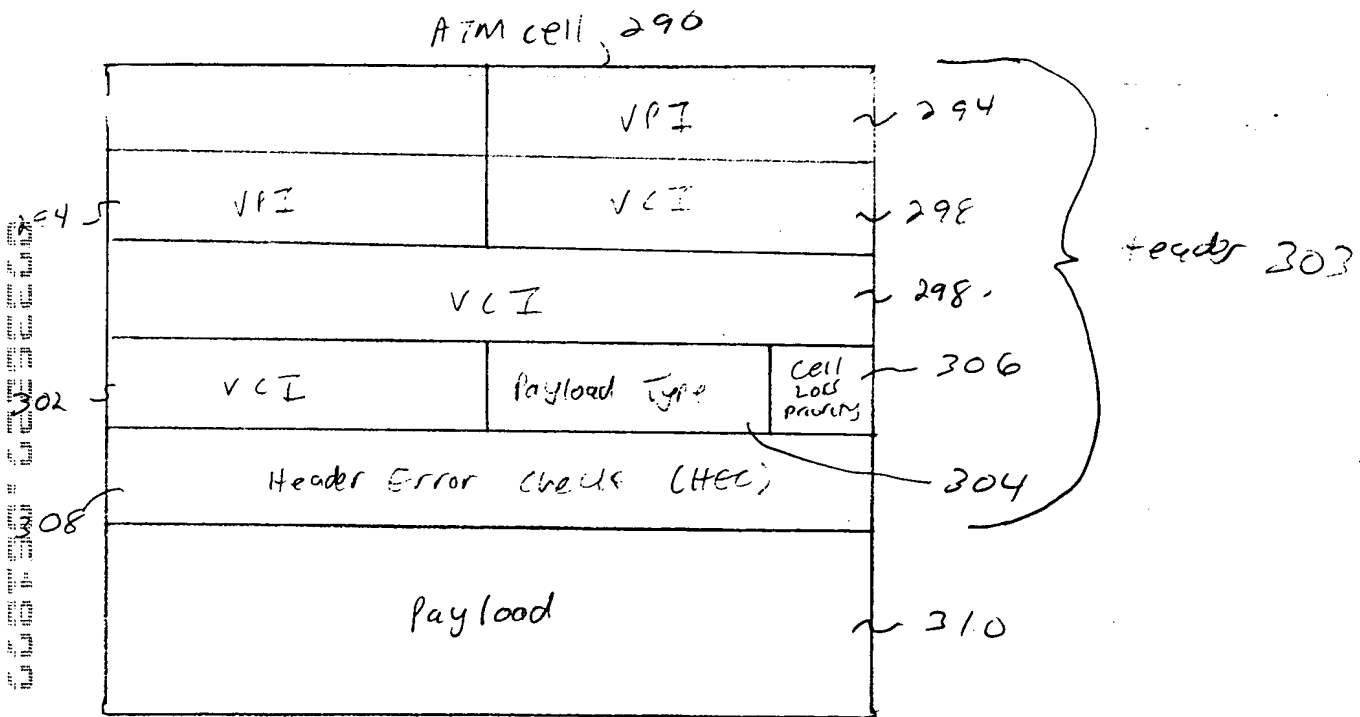


Figure 17

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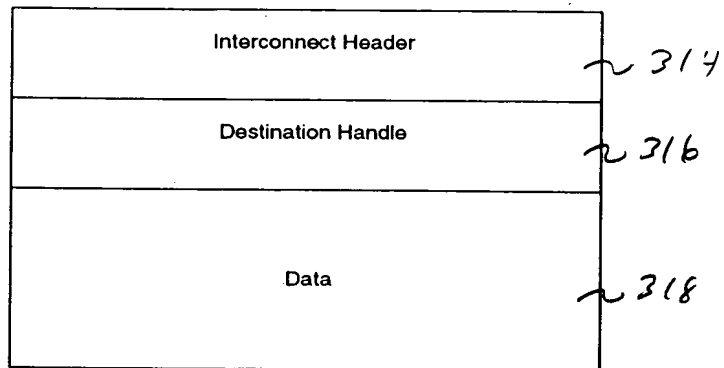


Figure 18

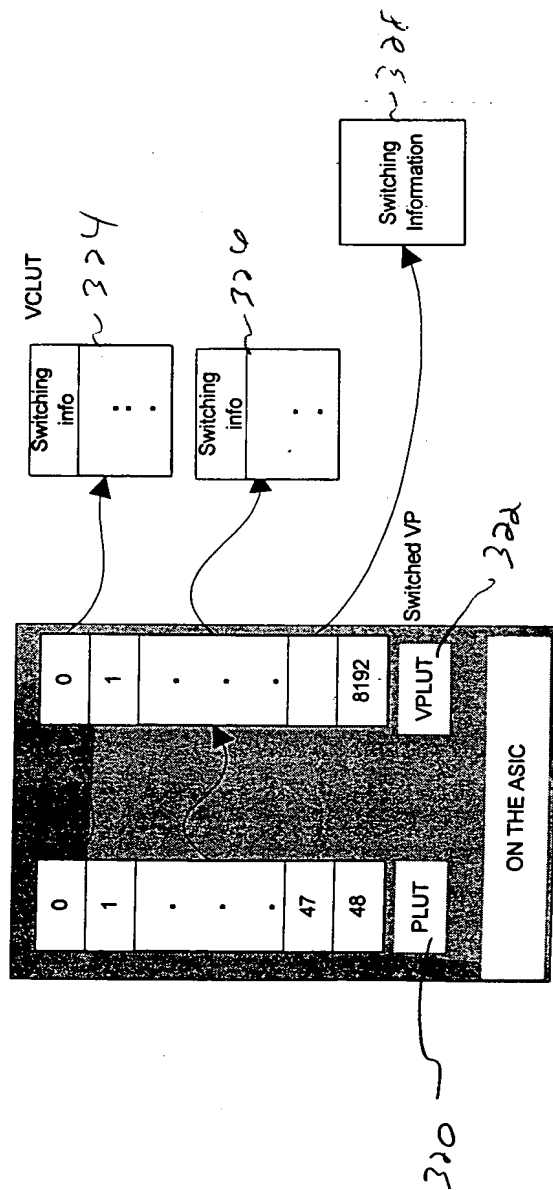
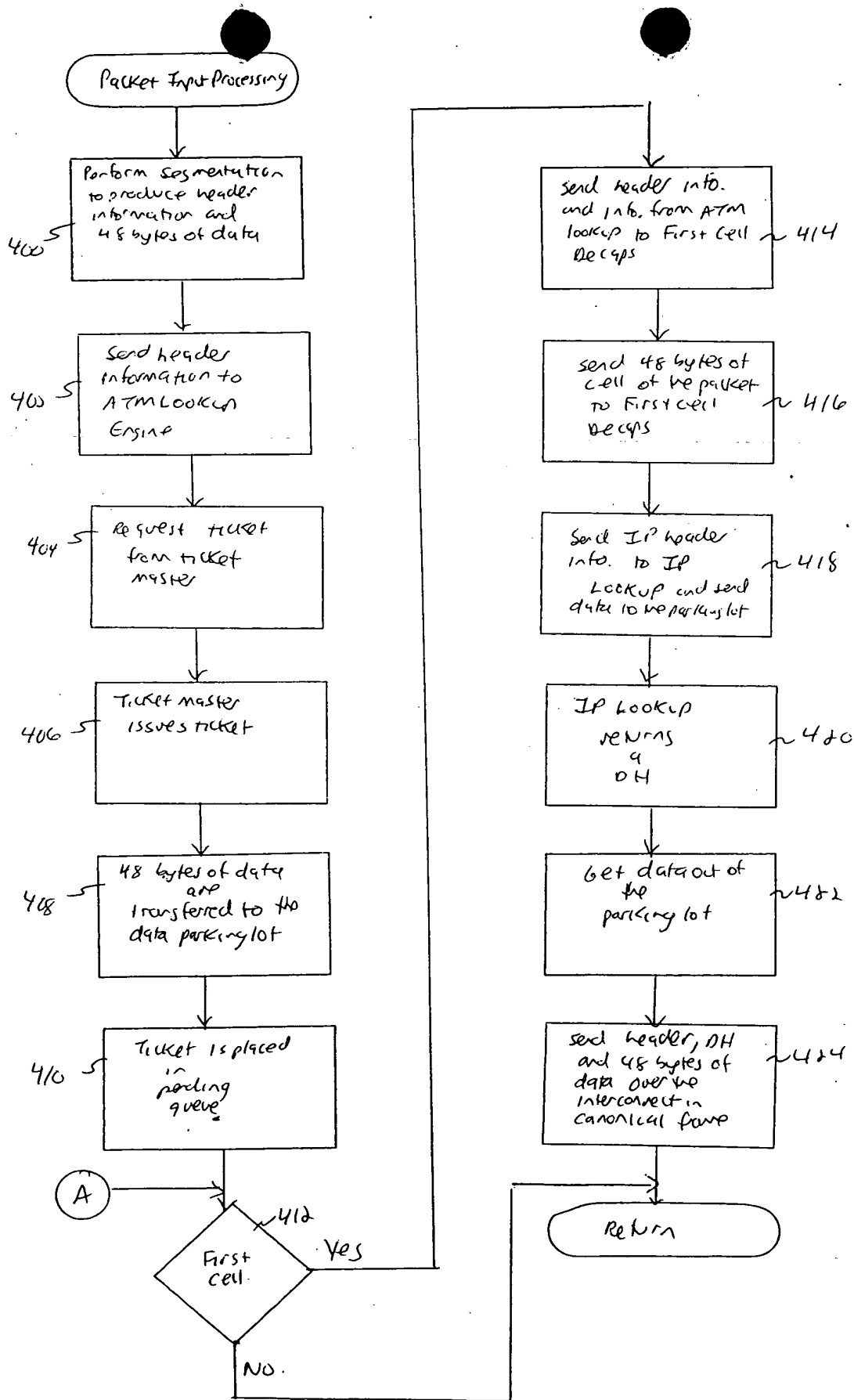


Figure 19



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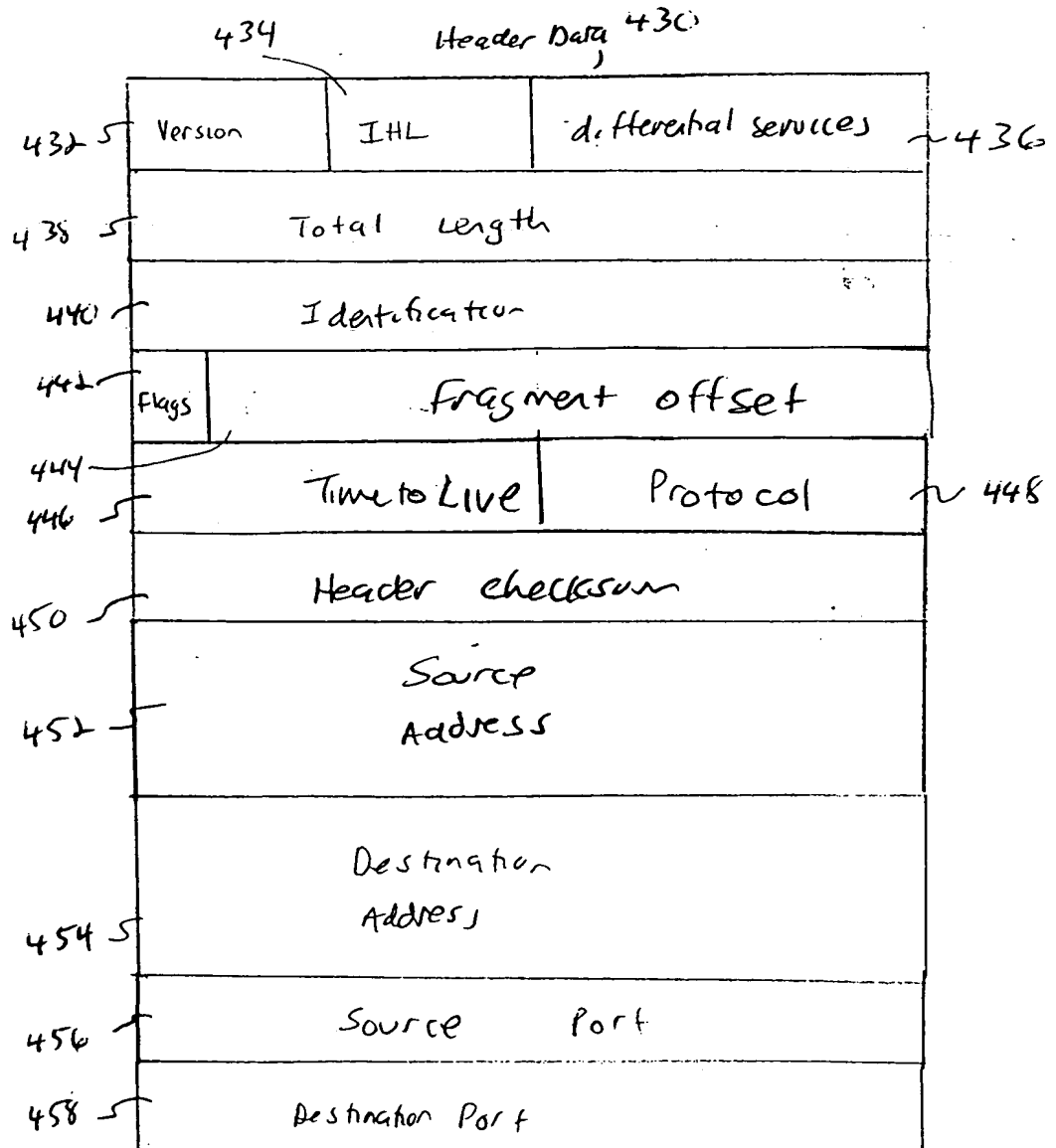


Figure 21

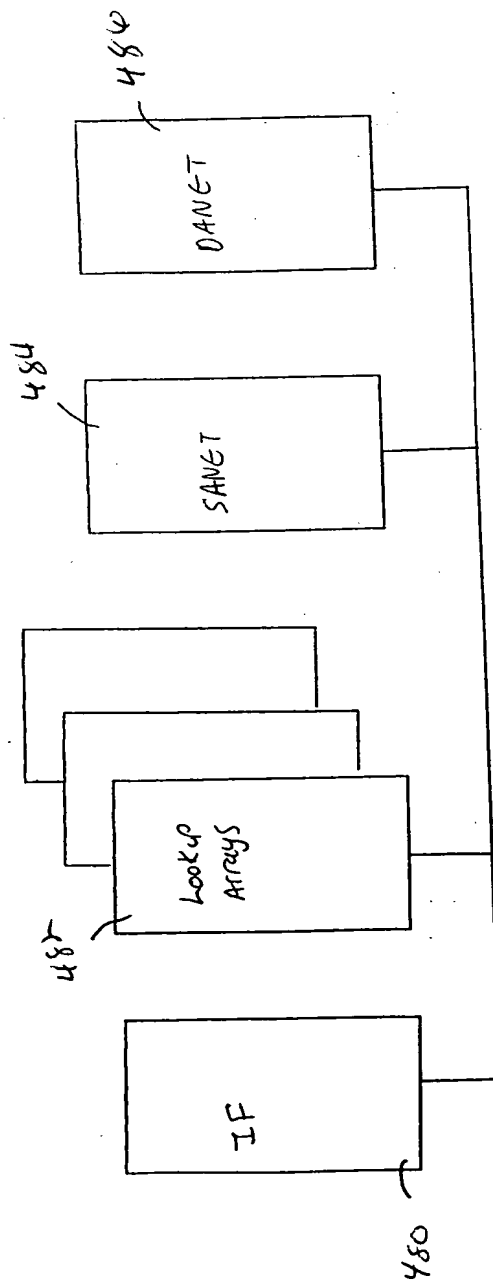


Figure 22

DANET STRUCTURE 486

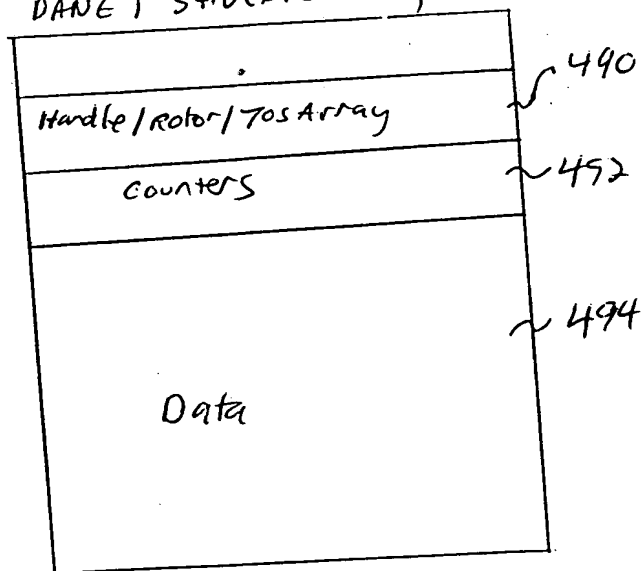


Figure 23

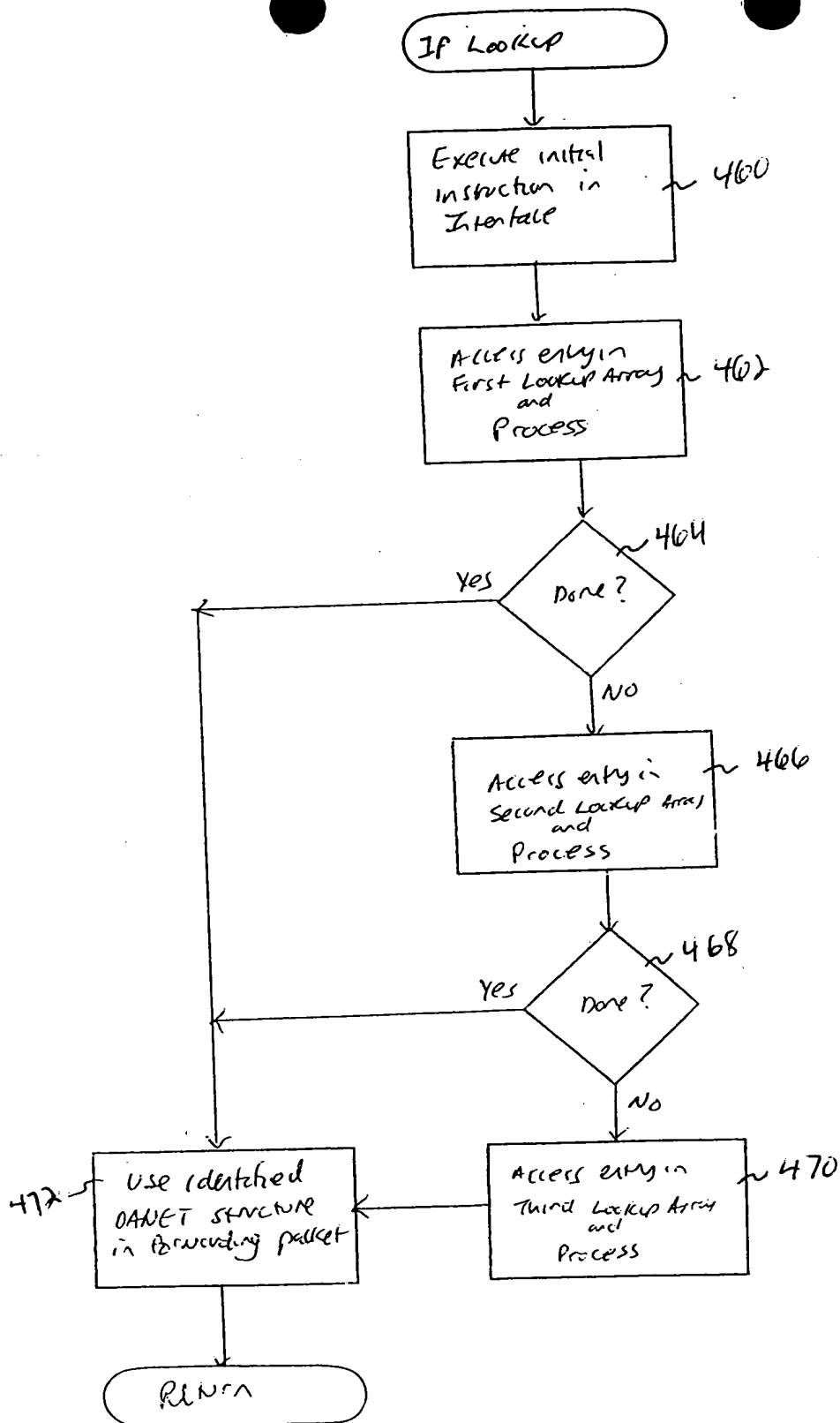
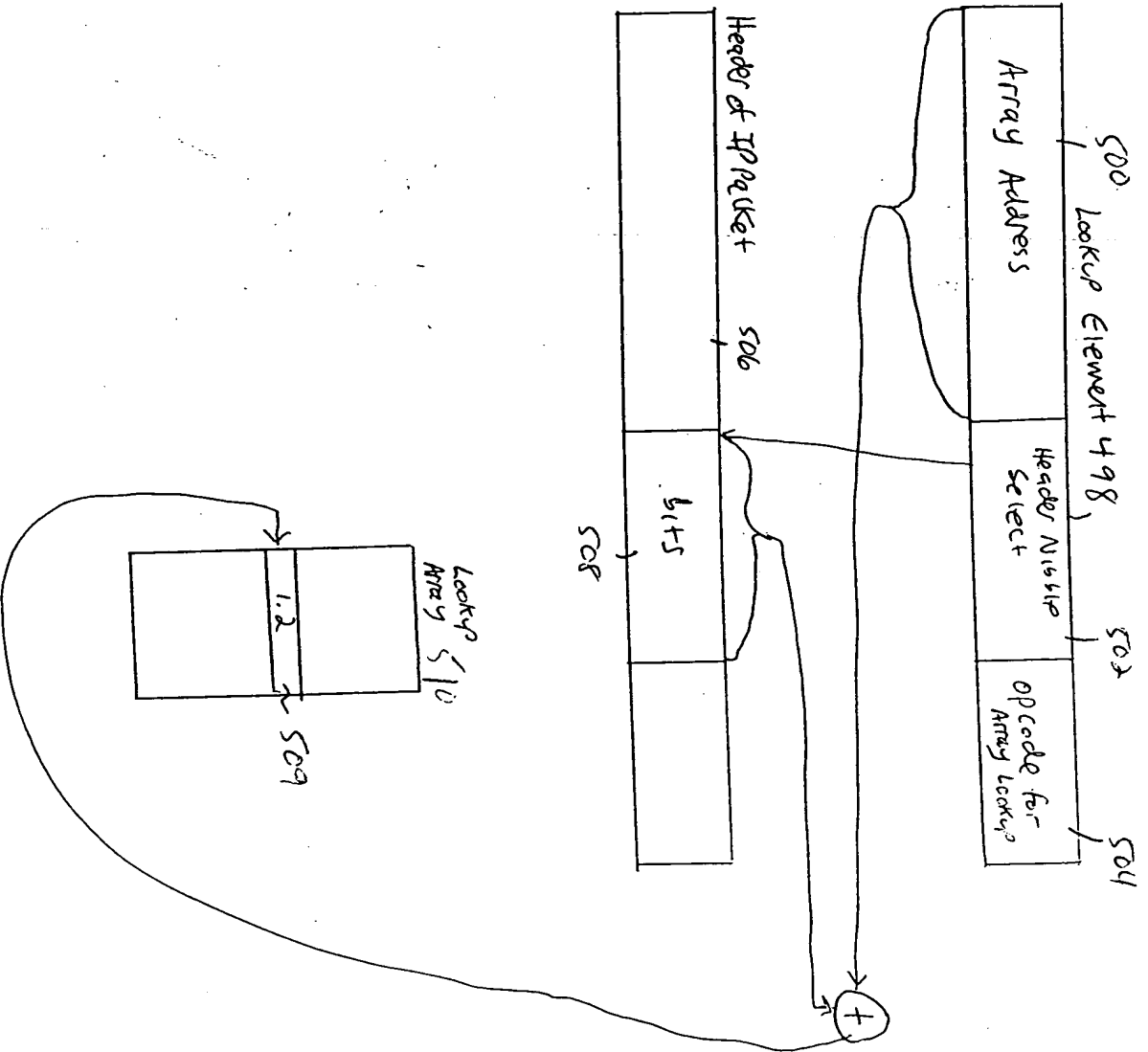
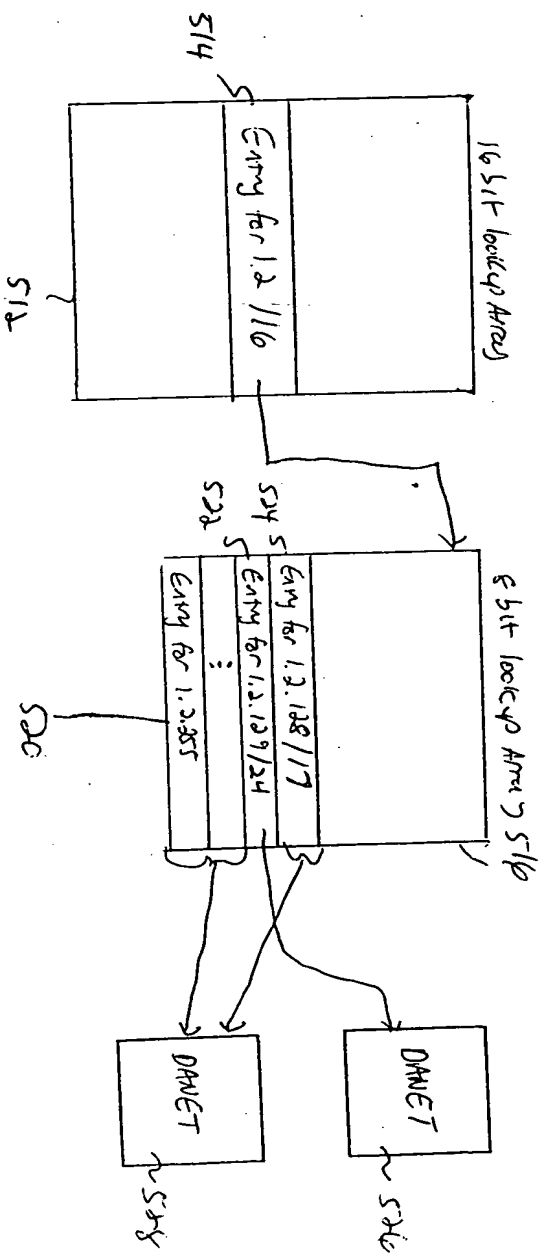


Figure 24





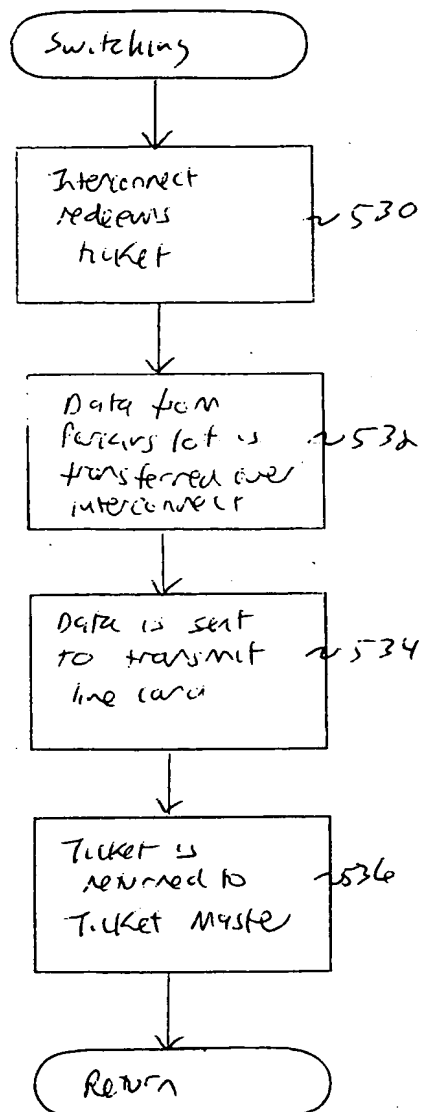
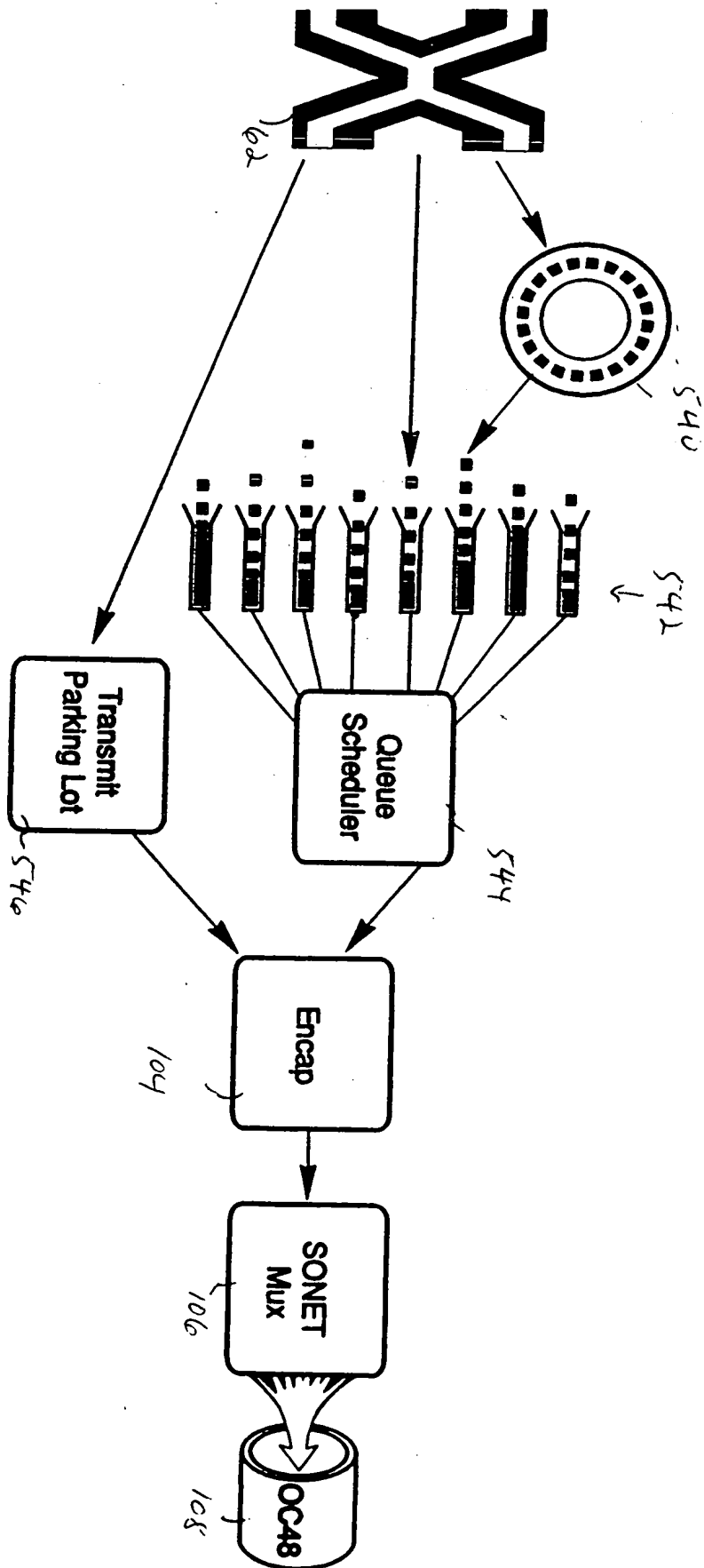
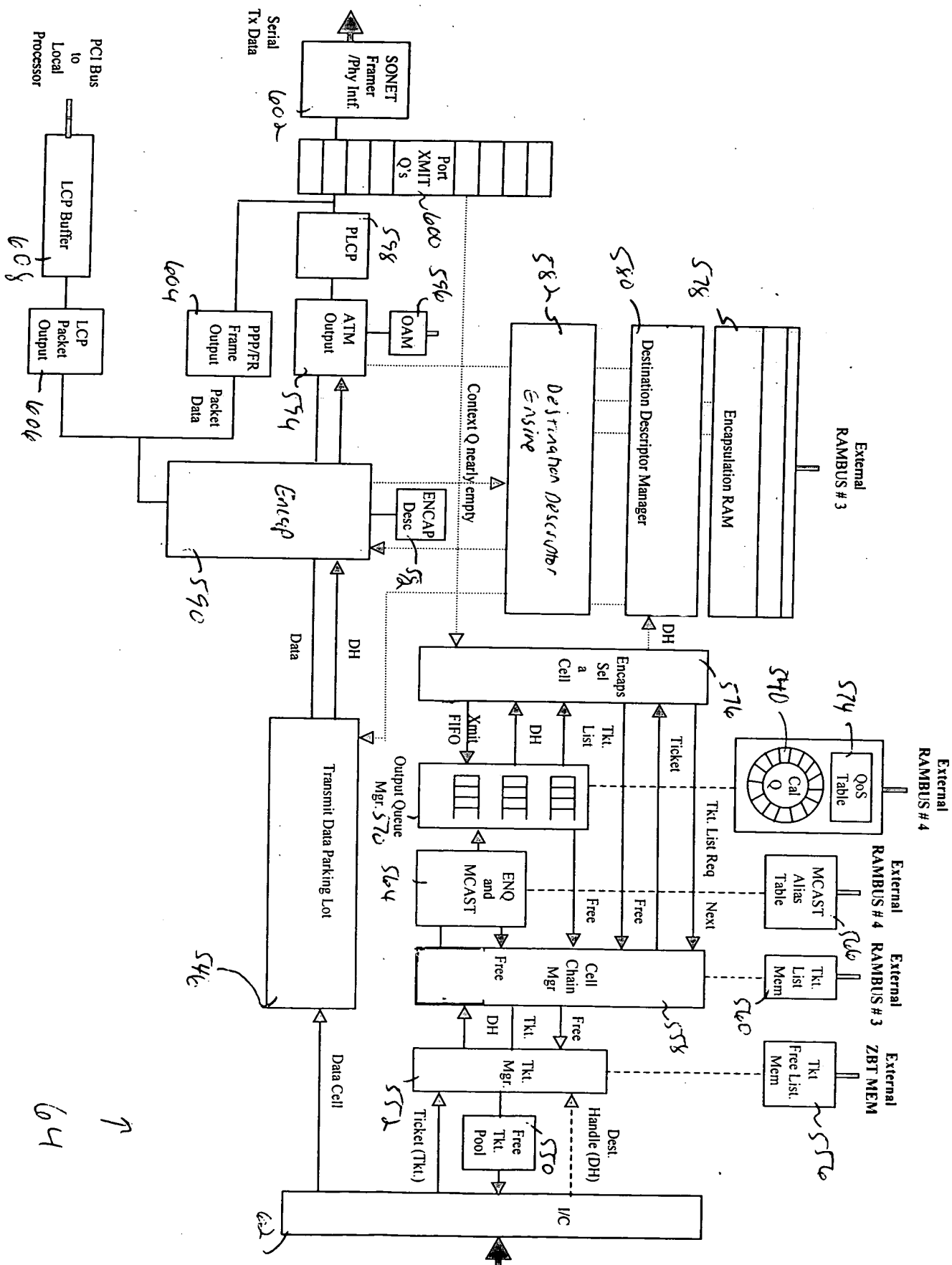
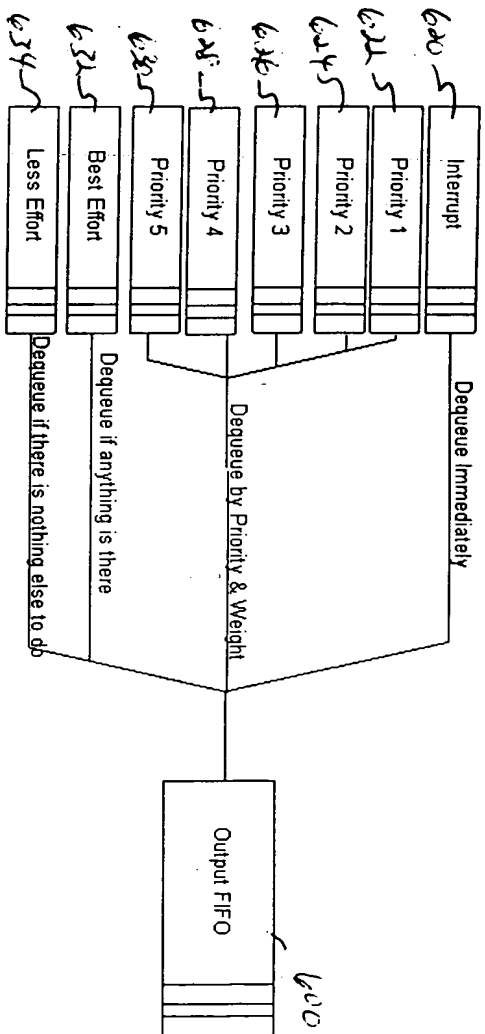


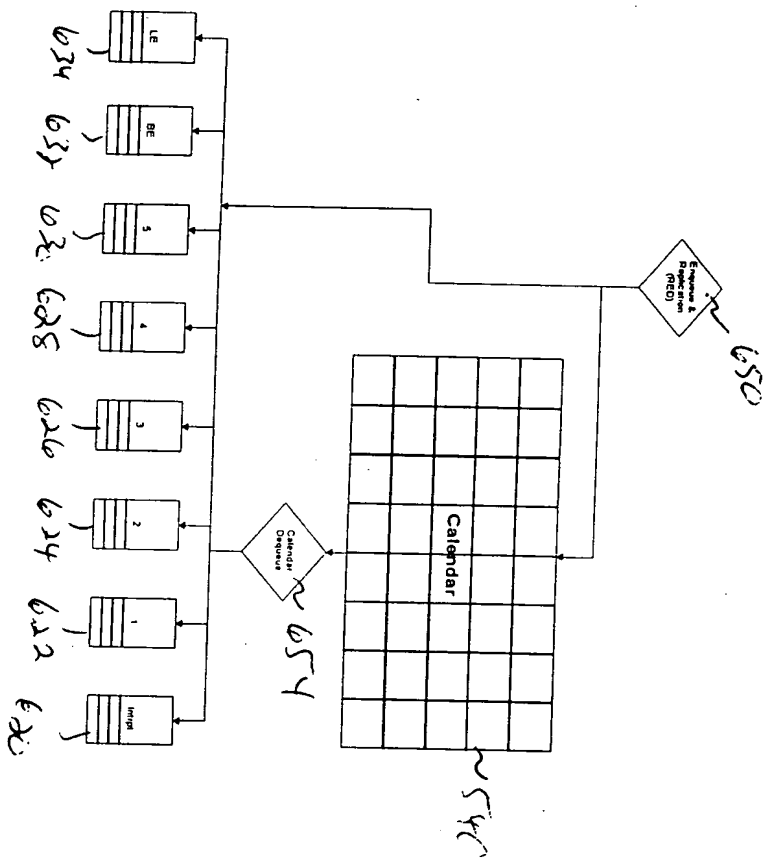
Figure 37







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